
Intergroup Switch and System Clock Generator

IGS messages

IGS0501 c Requested clock c card failed to respond.

IGS0502 c Requested clock c card failed self test.

IGS0503 c s loop Phase locked on primary source loop, DTI shelf s, and the Clock controller c is active.

IGS0504 c s loop Phase locked on secondary source loop, DTI shelf s, and the Clock controller c is active.

IGS0505 c s loop Unable to track on primary source loop, DTI shelf s and the Clock Controller c is active.

IGS0506 c s loop Unable to track on secondary source loop, DTI shelf s, and the Clock controller c is active. Free running.